



08/147038

628.30050CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): R. C. FOSS et al

Serial No.: (This is a continuation of application
Serial No. 07/680,747)

Filed: November 4, 1993

For: METHOD FOR DRAM SENSING
CURRENT CONTROL

Group: 2502

Examiner: T. Dinh

Pre Amdt

15/c

12-30-93

TJ

PRELIMINARY AMENDMENTHonorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

November 4, 1993

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please amend claim 1 as follows:

1. (twice amended) A dynamic random access memory (DRAM) comprising:
 - (a) a plurality of bit storage capacitors,
 - (b) a folded bit line comprised of a complementary bit line pair for receiving charge stored on one of said capacitors, having bit line capacitance,
 - (c) a sense amplifier having a pair of sense nodes for sensing a voltage differential across said sense nodes,

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